Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1803	716/7,12,16.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 08:23
L2	0	(cost with product adj term).clm.	US-PGPUB	OR	ON	2005/11/28 08:23
S1	413	input adj width	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:08
S2	44	S1 same logic	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 14:48
S3	12	S2 and ($1PLA or $1PLD or FPGA)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:01
S4	544	input adj width	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 14:50
S5	7	S4 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 14:49
S6	9	S4 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:49
S7	646	input adj width	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:05
S8	9	S7 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:49
S9	0	S8 and ($1PLA or $1PLD or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:50
S10	646	input adj width	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:50
S11	31	S1 and ($1PLA or $1PLD or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:50

S12	54	S10 and ($1PLA or $1PLD or FPGA)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:51
S13	38	S12 and cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 14:51
S15	20336	logic adj block	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:56
S16	5	S15 same S10	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:15
S17	18444	product near2 term	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 16:00
S18	411	S17 with cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:15
S19	5	S18 same S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:39
S20	662	input same (wider or larger or maximum) same S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:40
S21	139	input with (wider or larger or maximum) with S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:43
S22	1	S21 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:41
S23	6	S20 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:42
S24	3620	input with (wider or larger or maximum) with logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:43

S25	163	S24 same S15	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:43
S26	85	S25 and cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:43
S27	1	S25 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:43
S28	18	S15 with feeder	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 15:56
S29	50	S17 with cascad$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 16:04
S30	22	S29 and ($1PLA or $1PLD or FPGA)	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:01
S31	17	S29 and S15	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:01
S32	22	S30 or S31	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:01
S33	121	S17 with (cascad$4 or expansion)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 16:05
S34	5	S33 same cost	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/22 16:06
S35	10304	input near2 width	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:08
S36	7	S35 same S17	USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/22 16:15
S37	18186	cost near3 function	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/11/28 08:22